



11/29 AF  
[10191/3602]

Applicant(s) : Jochen WEBER et al.  
Serial No. : 10/801,363  
Filed : March 15, 2004  
For : MICROPROCESSOR SYSTEM AND METHOD FOR  
DETECTING THE EXCHANGE OF MODULES OF THE  
SYSTEM  
Examiner : Fatoumata TRAORE  
Art Unit : 2436  
Confirmation No. : 3174

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Date: 7/29, 2009

Signature: 

Jong H. Lee (Reg. No. 36,197)

### **RULE 116 RESPONSE AFTER FINAL REJECTION**

SIR:

In response to the final Office Action of June 15, 2009, kindly consider the above-captioned application in view of the following.

**Amendments to the Claims** are not presented, but a listing of the claims beginning on page 2 of this paper is presented for the Examiner's convenience.

**Remarks** begin on page 6 of this paper.